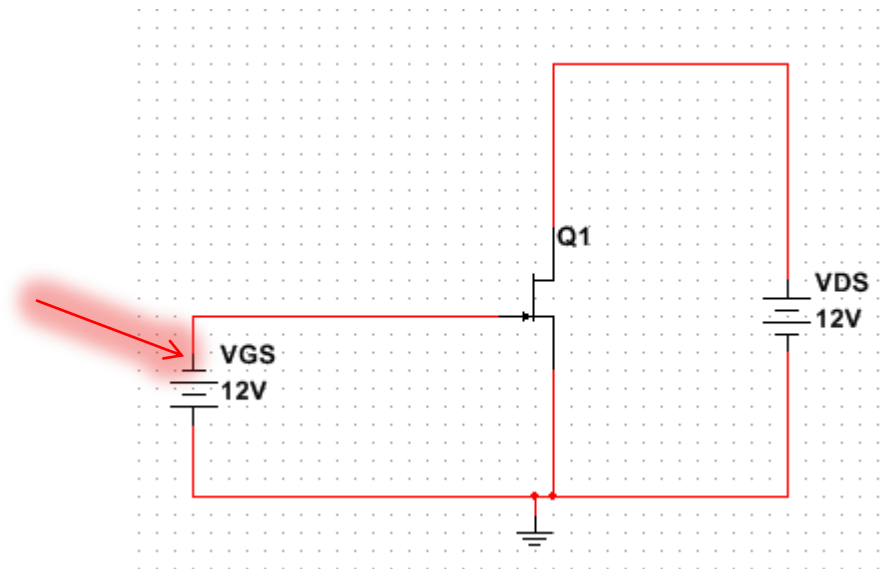


EXPERMENT 1 : I/V characteristics of JFET

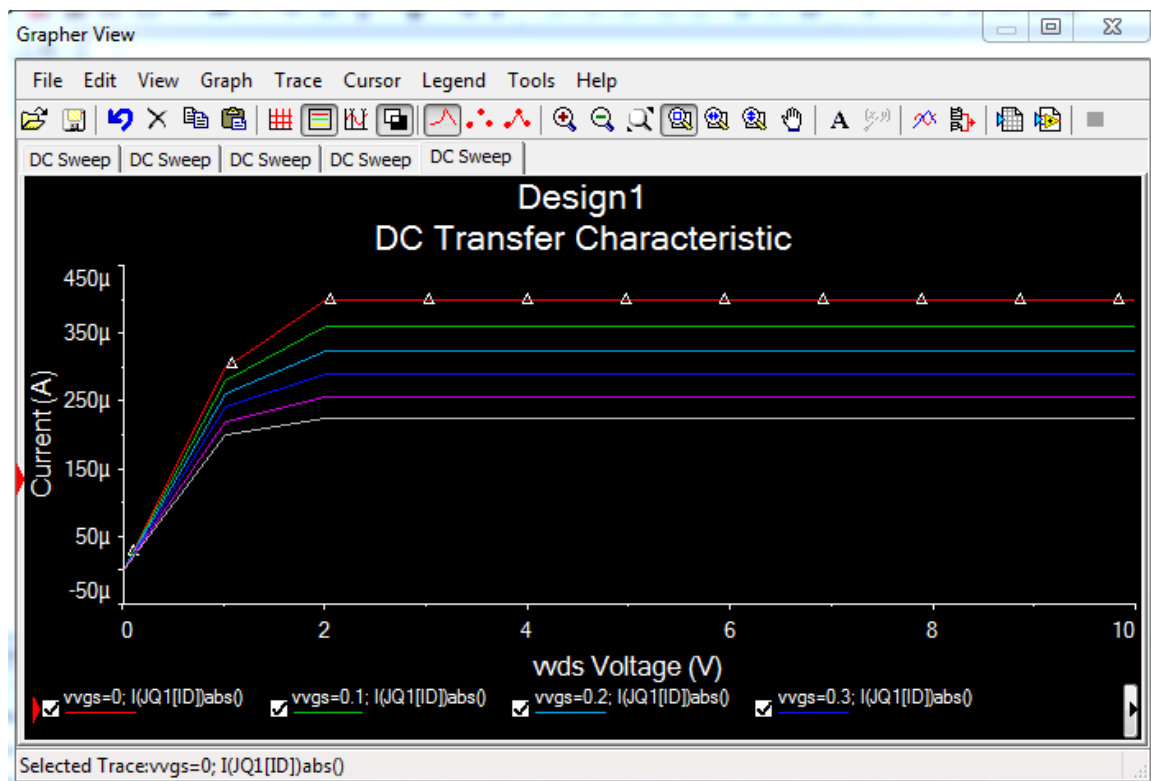
AIM: Study the relationship between drain current (I_D) and gate source voltage (V_{GS}).

APPARATUS: JFET (Virtual transistor), breadboard, 2 D.C power supply and Ground.

Circuit Diagram:



Output:



Procedure: